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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/076,757	02/14/2002	Steven Hauptman	1619-US	1786	
7590 03/01/2004			EXAMINER		
Legal Department			TERESINSKI, JOHN		
Teradyne, Inc. 321 Harrison Avenue			ART UNIT	PAPER NUMBER	
Boston, MA 02118			2858		
			DATE MAILED: 03/01/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)					
	10/076,757	HAUPTMAN, STE	VEN				
Office Action Summary	Examiner	Art Unit	-				
	John Teresinski	2858					
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the c	orrespondence ad	dress				
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1) Responsive to communication(s) filed on 21 N	ovember 2003.						
,	action is non-final.						
3) Since this application is in condition for allowa	nce except for formal matters, pro	secution as to the	e merits is				
closed in accordance with the practice under E	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) Claim(s) <u>1-8</u> is/are pending in the application.							
4a) Of the above claim(s) is/are withdra	wn from consideration.						
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1-8</u> is/are rejected.							
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/o	r election requirement.						
Application Papers							
9) The specification is objected to by the Examine	er.						
10) The drawing(s) filed on is/are: a) acc	epted or b) objected to by the	Examiner.					
Applicant may not request that any objection to the	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correc	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Ex	xaminer. Note the attached Office	Action or form P7	rO-152.				
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Burea * See the attached detailed Office action for a list	ts have been received. Is have been received in Applicat wity documents have been receiv u (PCT Rule 17.2(a)).	ion No ed in this National	Stage				
Attachment(s)	0 🗆	4/PTO 442\					
Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) 🔲 Interview Summary Paper No(s)/Mail D	/ (P10-413) late					
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	_ 🗖		O-152)				

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4 and 8 are rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 5,718,117 to McDunn et al..

Regarding claims 1 and 8, McDunn et al. disclose a printed circuit board assembly having:

a first circuit board (45) having a first device side, the first device side having a portion configured to mount a first plurality of semiconductor devices (column 4 lines 50-54);

a second circuit board (45) having a second side and disposed in confronting parallel relationship to the first circuit board (column 4 lines 50-54); and

a border (Fig. 5, elements 12, 14) interposed between the first and second boards and disposed around the first and second portions to form a liquid tight container with an inlet (52) to receive electrically nonconducting liquid and an outlet for discharging (column 4 lines 16-21, column 5 lines 22-28).

Regarding claim 2, McDunn et al. disclose channel cards for use in a semiconductor tester (column 6 lines 7-13).

Regarding claim 3, McDunn et al. disclose a border element having a thin metallic wall of a uniform height and respective top and bottom sealing edges (12, 14) and respective first and

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second seals disposed between the top and bottom sealing edges and the first and second device sides (column 5 lines 22-28, 36-38).

Regarding claim 4, McDunn et al. disclose the first and second printed circuit boards have devices mounted solely on the first and second device sides (column 4 lines 46-65).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 5-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over McDunn et al. in view of U.S. Patent No. 4,749,943 to Black.

Regarding claims 5 and 6, McDunn et al. teaches the printed circuit board assembly as described above but does not disclose a computer workstation or a testhead adapted for being carried by a manipulator. Black disclose that it is well known in the art to test printed circuit boards with a computer workstation and a testhead adapted for being carried by a manipulator (column 5 lines 31-58). It would have been obvious to one of ordinary skill in the art at the time the invention was made to include a computer workstation and testhead as taught by Black into McDunn et al. to ensure the correct functioning of electronic components loaded on the board (column 1 lines 15-25).

Regarding claim 4, McDunn et al. disclose the first and second printed circuit boards have devices mounted solely on the first and second device sides (column 4 lines 46-65).

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Response to Arguments

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Applicant's arguments with respect to claims 1-8 have been considered but are moot in

view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to John Teresinski whose telephone number is (571) 272-2235. The

examiner can normally be reached on M-F 8:30 - 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, N. Le can be reached on (571) 272-2233. The fax phone number for the organization

where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

February 6, 2004

Supervisory Patent Examiner

Technology Center 2800